

ABSTRACT

One of technique of evocation of sinyal FM is by using PLL. In communications system, PLL represent the peripheral which is often used. One of its application is as synthesizer frequensi. Named By PLL(Phase Locked Loop) as synthesizer because this circuit able to yield a lot of frequency with one input frequency / reference, where condition of frequency of output PLL will always follow the its reference frequency arranged by a[n divisor or pogrammable divider

At this final project have been designed a circuit PLL yielding range frequency (88-108)MHz by using LED indicator in area of frequency of lock and unlock. LED in this circuiti is used as signal detector lock and unlock, so that if output frequency is lock LED willoff and if frequency of output unlock hence LED of ON

Based on calculation result, got span the programmable divider capable to yield the frequency lock (88-108)MHz with the space of canal or resolution 200KH is 440-540 so that outside the setting, PLL stay in the condition Unlock.