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RISC	: <i>Reduced Instruction Set Computer</i>
FPGA	: Field Programmable Gate Array
SOC	: System On Chip
ASIC	: Application-Specific Integrated Circuit
LGPL	: Lesser General Public License
ORPSOC	: OpenRISC Reference Platform System On Chip
IC	: Integrated Circuit
USB	: Universal Serial Bus
ISA	: Instruction Set Architecture
ALU	: Arithmetic Logic Unit
LSU	: Load Store Unit
CISC	: Complex Instruction Set Computer
CPU	: Control Processing Unit
GPRs	: General Purpose Registers
HDL	: Hardware Description Language

OR1200	: Open RISC 1200
OR1K	: Open RISC 1000
MMU	: Memory Management Unit
ABI	: Application Binary Interface
DSP	: Digital Signal Processing
PIC	: Programmable Interrupt Controller
MAC	: Multiply-Accumulator
OR1KSIM	: OpenRISC 1000 Simulator
IOB	: Input/ Output Block
CLB	: Configurable Logic Block
PAR	: Place And Route
MIPS	: Million Instruction Per Second
CPI	: Cycles per Instruction
ROM	: Read Only Memory