DAFTAR PUSTAKA

[1] R. Oshana, *DSP for embedded and real-time systems*. Newnes, 2012.

[2] I. Verbauwhede, P. Schaumont, C. Piguet, B. Kienhuis, and K. U. Leuven, "Architectures and Design techniques for energy efficient embedded DSP and multimedia processing," pp. 1–6.

[3] D. M. Weste, Neil H. E. & Harris, *CMOS VLSI Design: A Circuits and Systems Perspective*, 4th ed. Addison Wesley, 2010.

[4] J. L. H. David A. Patterson, *Computer Organization and Design: The Hardware/Software Interface 4th ed*. Morgan Kaufmann, 2008.

[5] J. Zhang, "Design of Audio Signal Processing and Display System Based on SoC," no. Iccsnt, pp. 824–828, 2015.

[6] A. Savadi, "A Survey on Design of Digital Signal Processor," pp. 2483–2486, 2016.

[7] S. M. Rubin, "Using the ELECTRIC VLSI Design System," 2013.

[8] A. a K. Nielsen *et al.*, "Genetic circuit design automation.," *Science*, vol. 352, no. 6281, p. aac7341, 2016.

[9] C. Ms, "Peter Jamieson , Jonathan Rose Edward S . Rogers Sr . Department of Electrical and Computer Engineering University of Toronto," pp. 305–310, 2005.

[10] C. Maxfield, "Altera's Quartus II design software features Qsys System Integration Tool," May-2011.

[11] R. C. Jaeger, *Microelectronic Circuit Design*, Internatio. McGraw-Hill, 1997.

[12] R. F. Tinder, *Engineering Digital Design*. 2000.

[13] D. De and K. Prasad, "Basic Electronics," 2010.