DAFTAR PUSTAKA

[1] BabatPost, "Pasar Game Asia Tenggara Punya Pertumbuhan yang Paling Pesat Sedunia," *BabatPost,* 22 Januari 2017. https://www.babatpost.com/ [Diakses 10 Maret 2018].

[2] Antara News, "Industri Game Indonesia Hasilkan 190 Juta Dollar," *Antara News,* 29 Januari 2014. https://www.antaranews.com/ [Diakses 10 Maret 2018].

[3] *Pacman* Web, "*What is Pacman*", *Pacman Web,* 28 November 2010 http://pacman.com/ja/whats/. [Diakses 18 Maret 2018]

[4] S. M. Trimberger, D. McCarty, and T. Whitney, *Field-programmable gate array technology*. 1994.

[5] M. Andrew, W. Ron. *FPGA for Dummies,* 2nd *Special Edition,.* Hoboken, NJ, 2017.

[6] Jatmiko, W., Mursanto, P., Fajar, M., Tawakal, M. I., Trianggoro, W., Rambe, R. S., Ramadhan A. *Implementasi Berbagai Algoritma Neural Network Dan Wavelet Pada Field Programmable Gate Array*. Januari 2011

[7] Brown, S. D., Francis, R. J., Rose, J., Vranesic, Z. G. *Field-Programmable Gate Arrays THE KLUWER INTERNATIONAL SERIES*. 1992

[8] Terry, L., Roitch, V., Tufail, S., Singh, K., Taraq, O., Luk, W., & Jamieson, P. *Harnessing Human Computation Cycles for the FPGA Placement Problem*, *Proceedings* - presented at the 2009 International Conference on Engineering of Reconfigurable Systems & Algorithms, Las Vegas, Nevada, USA Juli 2009

[9] Sharma, S., Gangwar, D. S., Scholar, M. T., & Design, V. *Design and Implementation of FPGA Based VGA Monitor and PS2 Keyboard Interface Technique*, *Imperial Journal of Interdisciplinary Research (IJIR),* vol. 3, issue 6, 2017

[10] Sharma, S., & Gangwar, D. S. *A Review on Design and Implementation of FPGA Based VGA Monitor and PS2 Keyboard Interface Technique*, *Imperial Journal of Interdisciplinary Research (IJIR),* vol. 3, issue 6, 2017.

[11] Zineb, L., Hayet, E., "FPGA Based VGA Controller and Arcade Games," M.S. thesis, University M'Hamed, Bougara, Boumerdes, Algeria, 2016

[12] Murthy, N. V. S. *Video Graphics Array interfacing through Artix-7 FPGA*, *International Research Journal of Engineering and Technology (IRJET)*, vol. 3, issue 3, Maret 2016.

[13] Altera Corporation. "Development and Education Board User Manual", *Altera Corporation,* 2012, https://www.terasic.com [Diakses 31 Maret 2018]

[14] Ramos-Arregu'n, C. A., Morales, J. C. M., Ramos-Arregu'n, J. M., Pedraza-Ortega, J. C., Tovar-Arriaga, S., Fernandez, M. A. A., & Rangel-Magdaleno, J. D. J. (2012). FPGA Open Architecture Design for a VGA Driver. *Procedia Technology*, *3*(December), 324–333.

[15] Susan, S. (2015). FPGA Implementation of Elevator Controller, (Icettas), 5–8.

[16] Chu, P. P. *Embedded SoPC Design with Nios II Processor and Verilog Examples*., Hoboken, NJ, 2011

[17] Gadget Factory. "LogicStart Shield", *Gadget Factory,* 2012, http://papilio.cc/ [Diakses 13 Februari 2019]