## BIBLIOGRAPHY

- N. Karna, N. Fatihah, and D. Kim, "generic," in 2019 International Conference on Information and Communication Technology Convergence (ICTC), 2019, pp. 612–616.
- [2] P. Deshmane, M. Lad, and P. Mhetre, "8 Bit Microprocessor Using VHDL," vol. III, no. Iv, pp. 241–246, 2014.
- [3] G. R. Gare and K. P. A. L. R, "Custom 8 Bit Microprocessor Designing and Implementation on FPGA Board," pp. 113–118, 2016.
- [4] R. Praisline Jasmi, B. Perumal, and M. Pallikonda Rajasekaran, "huffman," in 2015 International Conference on Computer Communication and Informatics (ICCCI), 2015, pp. 1–5.
- [5] N. Karna, N. Fatihah, and D. S. Kim, "Evaluation of DLX Microprocessor Instructions Efficiency for Image Compression," *ICTC 2019 - 10th International Conference on ICT Convergence: ICT Convergence Leading the Autonomous Future*, pp. 612–616, 2019.
- [6] H.-y. Shen, J.-h. Liu, and J.-h. Li, "Application of Quartus II in Digital Electronic Technology Teaching," no. Amee, pp. 103–106, 2018.
- [7] L. Isola, "Design and VHDL Implementation of an Application-Specific Instruction Set Processor," 2019.
- [8] R. J. Hayne, T. Citadel, R. J. Hayne, and C. Engi, "AC 2011-5 : AN INSTRUC-TIONAL PROCESSOR DESIGN USING VHDL An Instructional Processor Design using VHDL and an FPGA," 2011.