

LIST OF TABLES

| | | |
|-----|---|----|
| 3.1 | Description of Schematic Circuit on Hardware | 15 |
| 4.1 | Accuracy of ETA | 35 |
| 4.2 | Data processing speed of the objects | 36 |
| 4.3 | Data Processing Efficiency (pps) for the algorithms | 37 |