

## DAFTAR PUSTAKA

- [1] Cansiza, M. (2019). *Efficiency in RF Energy Harvesting Systems*.
- [2] Ma'ruf1, L. (2019). *PERANCANGAN PELIPAT TEGANGAN UNTUK SISTEM CATU DAYA PADA ELEKTROLISIS*.
- [3] mughni, m. m. (2020). *rancang bangun voltage multiplier DC dengan metode cockroft walton tipe fullwave*. jember: moh. mu'izzul mughni.
- [4] Nasution, M. (2021). *Karakteristik Baterai Sebagai Penyimpan*.
- [5] S Pourjafar, H. S. (2021). *A non-isolated high step-up DC-DC converter using magnetic coupling and voltage multiplier circuit*.
- [6] W Ali, H. S. (2022). *Wireless energy harvesting using rectenna integrated with voltage multiplier circuit at 2.4 GHz operating frequency*.
- [7] Wanda Afnison\* Erzeddin Alwi, H. M. (2020). *Pengembangan Electromagnetic Regeneratif Shock Absorber Sebagai Alat Pemanen Energy Getaran Pada Kendaraan*.
- [8] Yudo, T. G. (2019). *UNJUK KERJA RANGKAIAN PENGGANDA TEGANGAN UNTUK PELAYANAN LAMPU TL (TUBE LUMINATION)*.
- [9] Kalla, S., Tyagi, V., & Rana, V. (2022). A novel DC-DC switched capacitor based voltage multiplier. *Analog Integrated Circuits and Signal Processing*, 112(1), 19-28.
- [10] Rajora, R., Sharma, K., Gupta, L., & Sachdeva, A. (2023, April). CNTFET-based design of low power charge pump technique-based voltage multiplier. In *2023 IEEE Devices for Integrated Circuit (DevIC)* (pp. 442-445).
- [11] Mollajafari, M., Mohammadiazar, F., Akbari, E., Sheykhi, E., & Chaoui, H. (2022). A New Voltage-Multiplier-Based Power Converter Configuration Suitable for Renewable Energy Sources and Sustainability Applications. *Sustainability*, 14(24), 16698.
- [12] Diaz-Sanchez, A., Mateus-Ardila, J. C., Zamora-Mejia, G., Diaz-Armendariz, A., Rocha-Perez, J. M., & Moreno-Coria, L. A. (2021). A four quadrant high-speed CMOS analog multiplier based on the flipped voltage follower cell. *AEU-International Journal of Electronics and Communications*, 130, 153582.

- [13] Andres, B., Romitti, L., Dupont, F. H., Roggia, L., & Schuch, L. (2023). A high step-up isolated DC–DC converter based on *voltage multiplier* cell. *International Journal of Circuit Theory and Applications*, 51(2), 557-578.
- [14] Mousavi-Aghdam, S. R., Dastgiri, A., & Hoseinpour, M. (2024). An improved voltage gain bidirectional DC/DC converter based on *voltage multiplier* cell for electric vehicle application. *International Journal of Modelling and Simulation*, 1-22.
- [15] Babanezhad, A., & Beiranvand, R. (2022, February). A Multiphase High Step-Up Interleaved Boost Converter with *Voltage Multiplier* and Active Clamp Circuits. In *2022 13th Power Electronics, Drive Systems, and Technologies Conference (PEDSTC)* (pp. 218-223).
- [16] Yang, X., Qi, Y., Liu, J., Jia, Z., & Wang, D. (2021). Bidirectional converter integrating voltage equalizer based on symmetrical *voltage multiplier* by sharing a magnetic component for series-connected cells. *Transactions on Transportation Electrification*, 7(3), 1074-1087.
- [17] BAKO, A., GARBA, I. I., AMBURSA, S. M., & LAWAL, F. (2024). High Step-Up SEPIC Converter with Double *Voltage Multiplier* Module for a Photovoltaic System.
- [18] Eskandari, A., Takarli, R., & Vahedi, A. A Novel High-Voltage Power Supply Using Lccl Resonant Dc-Dc Converter. Available at SSRN 5082755.
- [19] Appikonda, M., & Kaliaperumal, D. (2024). Dynamic performance analysis of DC–DC boost converter with one-and two-*voltage multiplier* cells for high voltage gain and voltage regulation. *Electrical Engineering*, 107(1), 65-77.
- [20] Li, J., Tang, Z., & Mahmoud, H. A. (2024). An efficient ternary multiplier for enhanced on-chip AI wearable systems using graphene nanoribbon field-effect transistors. *AEU-International Journal of Electronics and Communications*, 155658.