

DAFTAR PUSTAKA

- [1] B. M. Gomez, "Hardware-in-the-Loop Simulation," vol. 2001, pp. 1–10, 2001.
- [2] C. Yang, Y. Xue, X. P. Zhang, Y. Zhang, and Y. Chen, "Real-time FPGA-RTDS co-simulator for power systems," *IEEE Access*, vol. 6, pp. 44917–44926, 2018, doi: 10.1109/ACCESS.2018.2862893.
- [3] F. Bounini, D. Gingras, V. Lapointe, and D. Gruyer, "Poster: Real-time simulator of collaborative autonomous vehicles," *Proc. 2014 Int. Conf. Adv. Comput. Commun. Informatics, ICACCI 2014*, pp. 1991–1994, 2014, doi: 10.1109/ICACCI.2014.6968630.
- [4] S. S. Thakur, G. O. H. Peng, J. K. Banda, S. Dasgupta, M. Husband, and W. Changyun, "Stability Analysis of the Electrical Power Generation System for a More Electric Aircraft," *2018 IEEE Energy Convers. Congr. Expo. ECCE 2018*, pp. 509–516, 2018, doi: 10.1109/ECCE.2018.8557655.
- [5] B. B. Wicaksono and T. Wati, "Load Frequency Control in Single-Area Power System Using Integral Control and Proportional Integral," *012 Int. Symp. Innov. Intell. Syst. Appl.*, vol. 2, no. 1, pp. 7–10, 2020.
- [6] Dillip Khamari, "Load Frequency Control of a Single Area Power System using Firefly Algorithm," *Int. J. Eng. Res.*, vol. V9, no. 05, pp. 1318–1320, 2020, doi: 10.17577/ijertv9is050825.
- [7] E. Challenges, "Hardware-in-the-Loop Simulations : A Historical Overview of," *Mdpi*, 2022.
- [8] M. Maniatopoulos, D. Lagos, P. Kotsampopoulos, and N. Hatziaargyriou, "Combined control and power hardware in-the-loop simulation for testing smart grid control algorithms," *IET Gener. Transm. Distrib.*, vol. 11, no. 12, pp. 3009–3018, 2017, doi: 10.1049/iet-gtd.2016.1341.
- [9] R. Saralegui, A. Sanchez, and A. de Castro, "Efficient Hardware-in-the-Loop Models Using Automatic Code Generation with MATLAB/Simulink,"

Electron., vol. 12, no. 13, 2023, doi: 10.3390/electronics12132786.

- [10] P. Kundur, "Power System Stability and Control - Prabha Kundur - McGraw-Hill Education." p. 1176, 1994. [Online]. Available: <https://www.mheducation.co.in/html/9780070635159.html>
- [11] J. Machowski, J. W. Bialek, and D. J. Bumby, *Power System Dynamics: Stability and Control*, 2nd ed. John Wiley & Sons, 2011.
- [12] N. Hatziargyriou *et al.*, "Definition and Classification of Power System Stability - Revisited & Extended," *IEEE Trans. Power Syst.*, vol. 36, no. 4, pp. 3271–3281, 2021, doi: 10.1109/TPWRS.2020.3041774.
- [13] H. Saadat, *POWER SYSTEM ANALYSIS*. Kevin Kane, 1999.
- [14] IEEE Std 1159, *Monitoring Electric Power Quality Developed by the Transmission and Distribution Committee IEEE Power and Energy Society*, vol. 2019. 2019. [Online]. Available: <http://www.ieee.org/web/aboutus/whatis/policies/p9-26.html>.
- [15] R. C. Dugan, S. Santoso, M. F. McGranaghan, and H. W. Beaty, *Electrical Power Systems Quality*, 2nd ed. McGraw Hill Professional, 2002.
- [16] V. Vittal, J. D. McCalley, P. M. Anderson, and A. A. Fouad, *Power System Control and Stability IEEE Press Series on Power and Energy Systems*, 3rd ed. John Wiley & Sons, 2019.
- [17] KESDM, "Aturan Jaringan Sistem Tenaga Listrik (Grid Code)," *Menteri Energi dan Sumber Daya Miner. Republik Indones.*, no. 3, pp. 417–607, 2020.
- [18] R. Barber, M. Horra, and J. Crespo, "Control practices using simulink with arduino as low cost hardware," *IFAC Proc. Vol.*, vol. 10, no. PART 1, pp. 250–255, 2013, doi: 10.3182/20130828-3-UK-2039.00061.
- [19] W. Ren, M. Steurer, and T. L. Baldwin, "Improve the stability and the accuracy of power hardware-in-the-loop simulation by selecting appropriate

interface algorithms,” *IEEE Trans. Ind. Appl.*, vol. 44, no. 4, pp. 1286–1294, 2008, doi: 10.1109/TIA.2008.926240.

- [20] B. Şenol and U. Demiroğlu, “Optimizing PI Controller for Stability and Overshoot in Step Response Using GA and PSO Techniques , A Comparative Study,” 2025, doi: 10.47897/bilmes.1578027.